

Antmicro DC-SCM board rework: ESPI/LPC pins, v1

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Objective is to bypass a no-pop voltage translator, U21. Without U21 populated, the ESPI pins on the DC-SCI connector are NC. On POWER9 platforms, the LPC bus is 3.3V, so the translator is not needed, and we can connect the DC-SCI ESPI pins (used for LPC in this case) directly to the Artix FPGA.

Original board design is from Antmicro, at <https://github.com/antmicro/artix-dc-scm> .

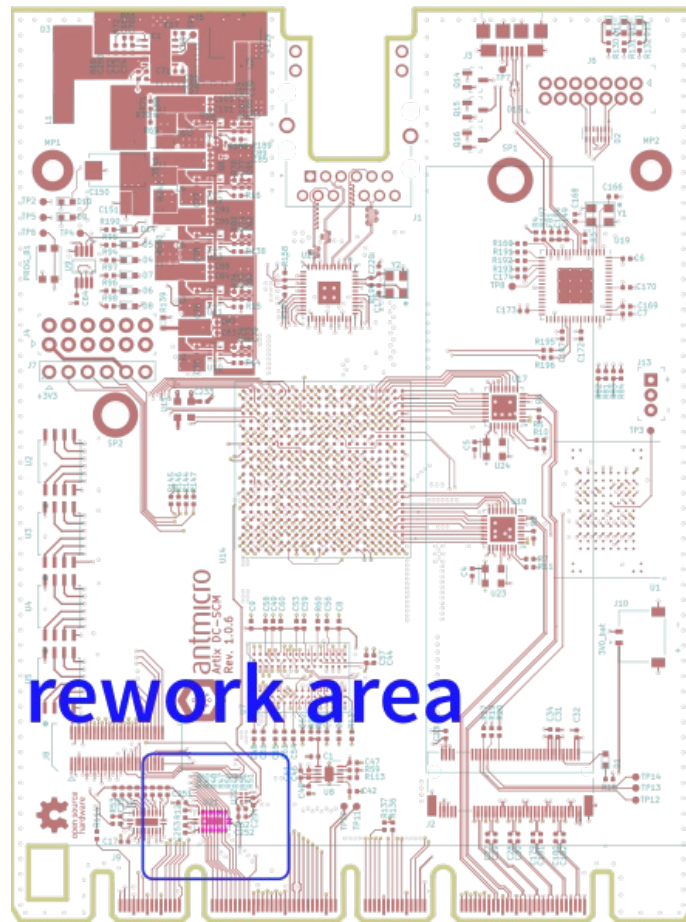
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Outline



General area is towards edge-connector side of board, component U21.

